

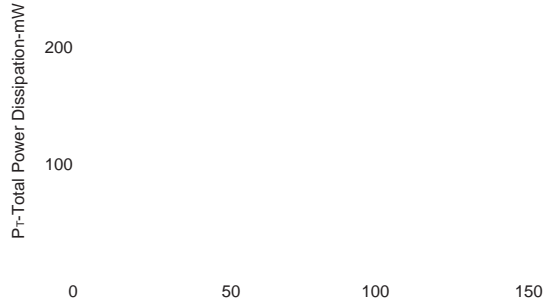
t		t	t
t t t		20	
t t tt t		12	
tt t t			
t t()		100	
t t	t t	200	
t t t	j	1 0	°C
t t t	t	- t + 1 0	°C

t		t t				t
t t t		10 , 0			1.0	μ
tt t t		1.0 , 0			1.0	μ
t *		10 , 20	0	120	2 0	
t	21 2	10 , 20 , 1			11.5	
		10 , , 1			1.2	2
t t **		10 , 0 , 1			0.	
t		10 , 20				GHz

* $t \leq 0 \mu$, $t \leq 2\%$.

** The emitter terminal and the case shall be connected to the guard terminal of the three-terminal capacitance bridge.

TOTAL POWER DISSIPATION vs.
AMBIENT TEMPERATURE



T_A -Ambient Temperature-°C DC
CURRENT GAIN vs. COLLECTOR
CURRENT

